

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/387,857	SUGAYA, FUMITAKA	
	Examiner	Art Unit	Page 1 of 1
	Christy L. Novacek	2822	

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,300,802	04-1994	Komori et al.	257/316
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S. and Tauber, R.N., "Silicon Processing for the VLSI Era", Vol. 1, pp. 407-408, 1986
	V	Wolf, S., "Silicon Processing for the VLSI Era", Vol. 2, pp. 65 and 203, 1990
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.